

ABSTRACT OF THE DISCLOSURE

A printed wiring board comprises an insulating layer having first and second surfaces and the wiring layers. The insulating layer has a via electrically connected between the wiring layers. The via has one end opened on the first surface and the other end closed by the wiring layer on the second surface. The inner surface of the via is covered with a first plating layer. The first plating layer continuously covers the wiring layer exposed within the via and that portion of the wiring layer which is formed on the first surface and which faces one end of the via. A second plating layer is laminated on the first plating layer. The second plating layer electrically connects the wiring layers by cooperating with the first plating layer.

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